



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Danial S. Dean

Serial No.: 10/633,956

Filed: August 4, 2003

For: DEVICE FOR MARGIN TESTING A SEMICONDUCTOR MEMORY BY APPLYING A STRESSING VOLTAGE SIMULTANEOUSLY TO

COMPLEMENTARY AND TRUE DIGIT

LINES

Confirmation No.: 5171

Examiner: Unknown

Group Art Unit: 2818

Attorney Docket No.: 2269-3318.6US

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 24, 2003

Date

Signature

Deidra J. Pheil

Name (Type/Print)

PRELIMINARY AMENDMENT

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Prior to examination of the above-referenced patent application on the merits, entry of the amendments as set forth herein is respectfully solicited.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing which begins on page 10 of this paper.

Remarks begin on page 11 of this paper.